

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 108391-00011	SERIAL NO. 09/652,023
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Yasuhiro WAKIMOTO	
		FILING DATE August 31, 2000	GROUP 2186

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>whc</i>	AA	5,802,554	9/1/1998	Cáceres et al			
<i>whc</i>	AB	5,845,311	12/1/1998	Piquet et al			
	AC						
	AD						
	AE						
	AF						

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO PART.		
	AG								
	AH								
	AI								
	AJ								
	AK								
	AL								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>whc</i>	AM	Edenfield, Robin, et al, "The 68040 On-Chip Memory Subsystem", Computer Society International Conference, IEEE Comp. Soc. Press, Vol. Conf. 35, February 1990, pp. 264-269 <i>whc</i> page 264
<i>whc</i>	AN	Fuccio, Michael L., "The DSP32C: AT&T's Second-Generation Floating-Point Digital Signal Processor", IEEE Micro, Vol. 8, No. 6, December 1998, pp. 30-47. <i>1st page, unknown page number whc</i>
	AO	

EXAMINER <i>[Signature]</i>	DATE CONSIDERED 3/29/04
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	